

UMB CS 420
Pushdown Automata (PDAs)

Wednesday, February 23, 2022



Announcements

- HW 3 in
- HW 4 out
 - Due Sun 2/27 11:59pm EST

Last Time: Generating Strings with a CFG

A **CFG** represents a **context free language!**

Strings in CFG's language
= all possible generated strings

$$G_1 =$$
$$A \rightarrow 0A1$$
$$A \rightarrow B$$
$$B \rightarrow \#$$

$$L(G_1) \text{ is } \{0^n \# 1^n \mid n \geq 0\}$$

Stop when string is all terminals

A CFG **generates** a string, by repeatedly applying substitution rules:

$$A \Rightarrow 0A1 \Rightarrow 00A11 \Rightarrow 000A111 \Rightarrow 000B111 \Rightarrow 000\#111$$

Start variable

Last Time:

Regular Languages	Context-Free Languages (CFLs)
Regular Expression	Context-Free Grammar (CFG)
A Reg Expr <u>describes</u> a Regular lang	A CFG <u>describes</u> a CFL

Today:

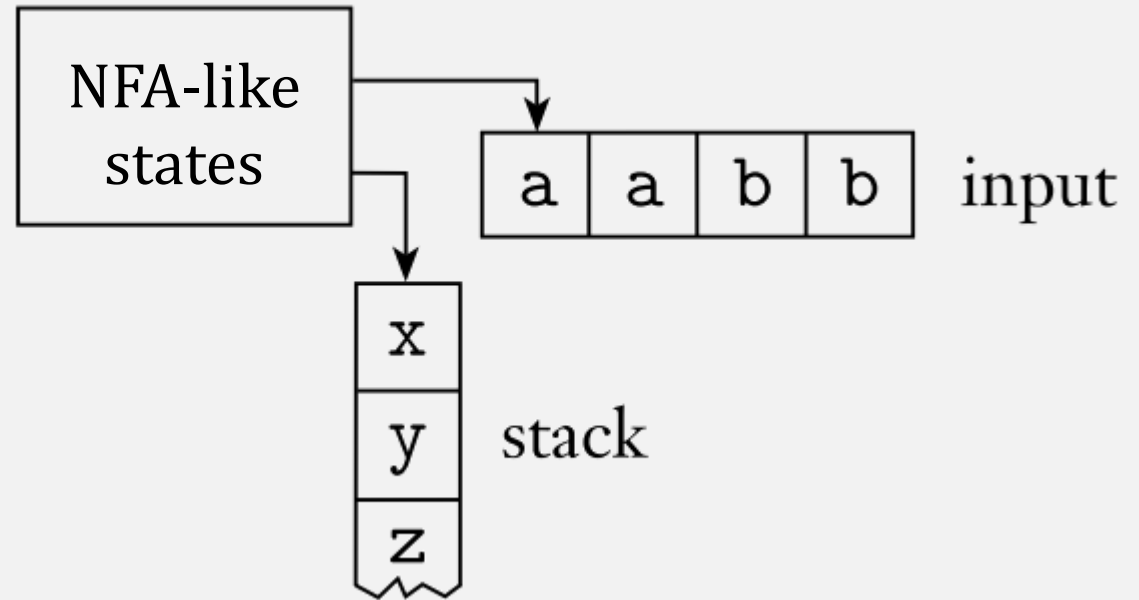
Regular Languages	Context-Free Languages (CFLs)
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A Reg Expr <u>describes</u> a Regular lang	A CFG <u>describes</u> a CFL
<u>TODAY:</u>	
Finite Automaton (FSM)	Push-down automaton (PDA)
An FSM <u>recognizes</u> a Regular lang	A PDA <u>recognizes</u> a CFL

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Finite Automaton (FSM)	Push-down automaton (PDA)
An FSM <u>recognizes</u> a Regular lang	A PDA <u>recognizes</u> a CFL
<u>KEY DIFFERENCE:</u>	
A Regular lang is <u>defined</u> with a FSM	A CFL is <u>defined</u> with a CFG
<i>Must prove: Reg Expr \Leftrightarrow Reg lang</i>	<i>Must prove: PDA \Leftrightarrow CFL</i>

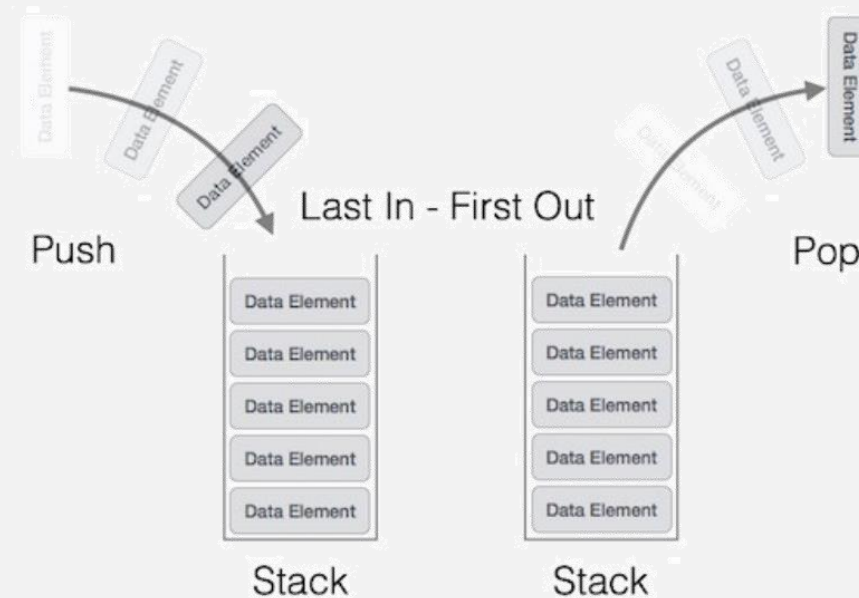
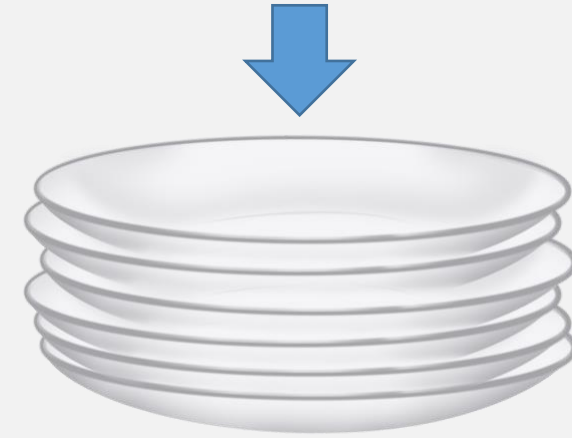
Pushdown Automata (PDA)

PDA = NFA + a stack



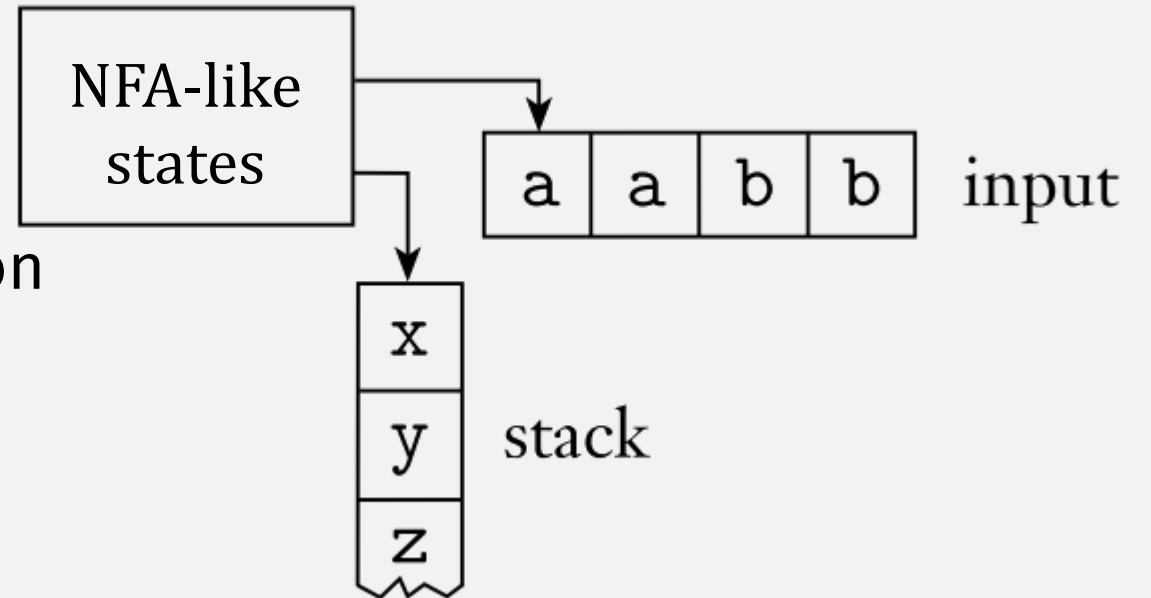
What is a Stack?

- A restricted kind of (infinite) memory
- Access to top element only
- 2 Operations only: push, pop



Pushdown Automata (PDA)

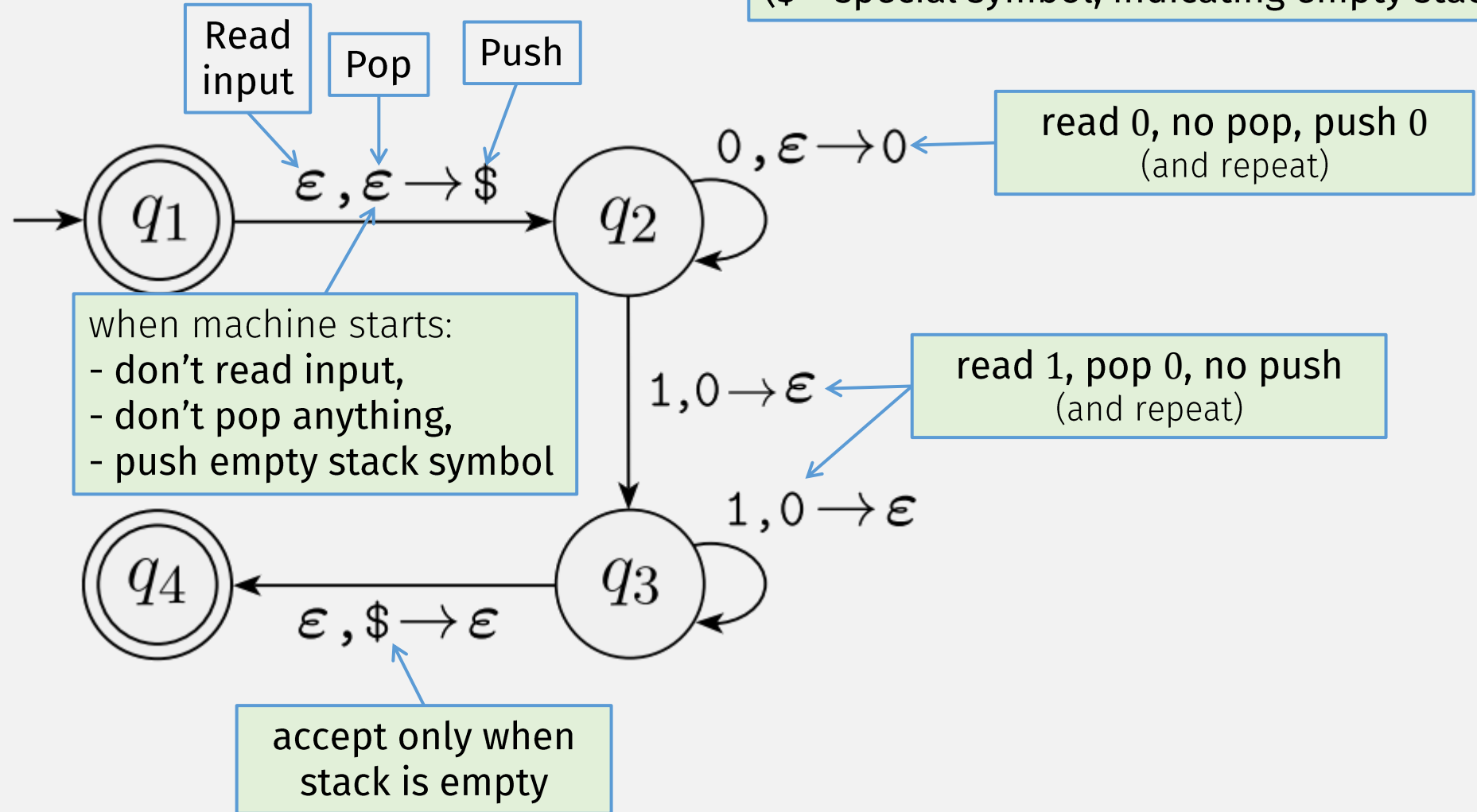
- PDA = NFA + a stack
 - Infinite memory
 - Can only read/write top location
 - Push/pop



$$\{0^n 1^n \mid n \geq 0\}$$

An Example PDA

(\$ = special symbol, indicating empty stack)



Formal Definition of PDA

A *pushdown automaton* is a 6-tuple $(Q, \Sigma, \Gamma, \delta, q_0, F)$, where $Q, \Sigma, \Gamma,$ and F are all finite sets, and

1. Q is the set of states,
2. Σ is the input alphabet,
3. Γ is the stack alphabet,
4. $\delta: Q \times \Sigma_{\epsilon} \times \Gamma_{\epsilon} \rightarrow \mathcal{P}(Q \times \Gamma_{\epsilon})$ is the transition function,
5. $q_0 \in Q$ is the start state, and
6. $F \subseteq Q$ is the set of accept states.

Stack alphabet can have special stack symbols, e.g., \$

Input Pop Push

Non-deterministic: produces a set of (STATE, STACK CHAR) pairs

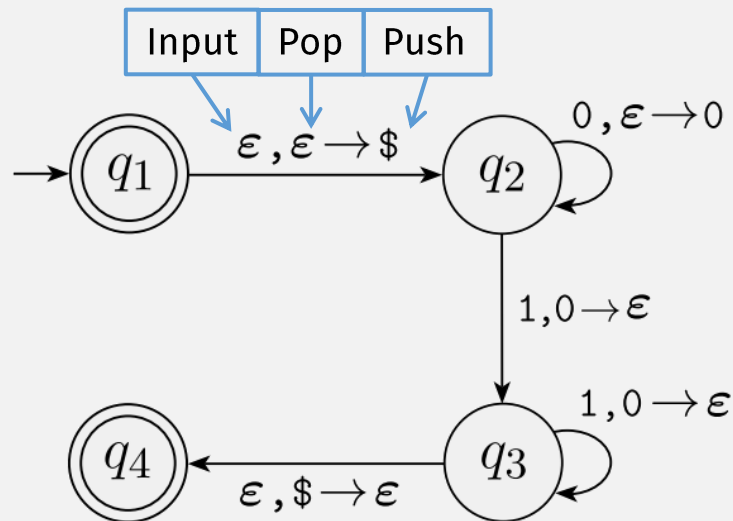
$$Q = \{q_1, q_2, q_3, q_4\},$$

PDA Formal Definition Example

$$\Sigma = \{0, 1\},$$

$$\Gamma = \{0, \$\},$$

$$F = \{q_1, q_4\},$$



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Input

Pop

Push

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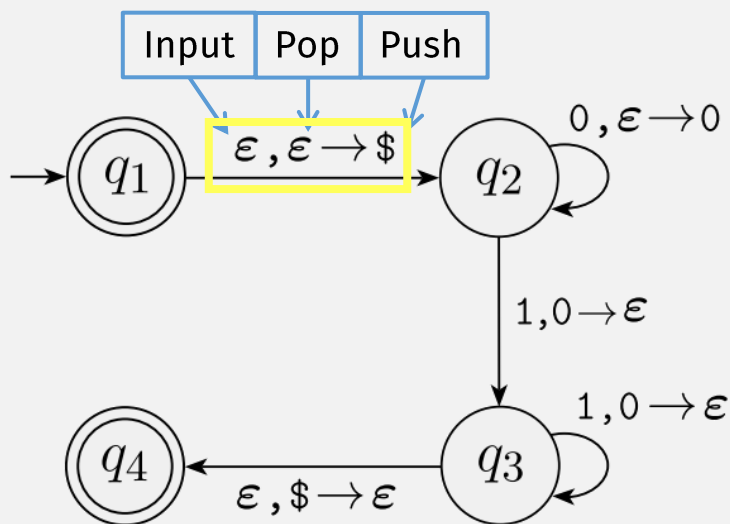
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$$F = \{q_1, q_4\}, \text{ and}$$

δ is given by the following table, wherein blank entries signify \emptyset .

Input:	0			1			ϵ		
Stack:	0	\$	ϵ	0	\$	ϵ	0	\$	ϵ
q_1									$\{(q_2, \$)\}$
q_2			$\{(q_2, 0)\}$			$\{(q_3, \epsilon)\}$			
q_3						$\{(q_3, \epsilon)\}$			
q_4									$\{(q_4, \epsilon)\}$



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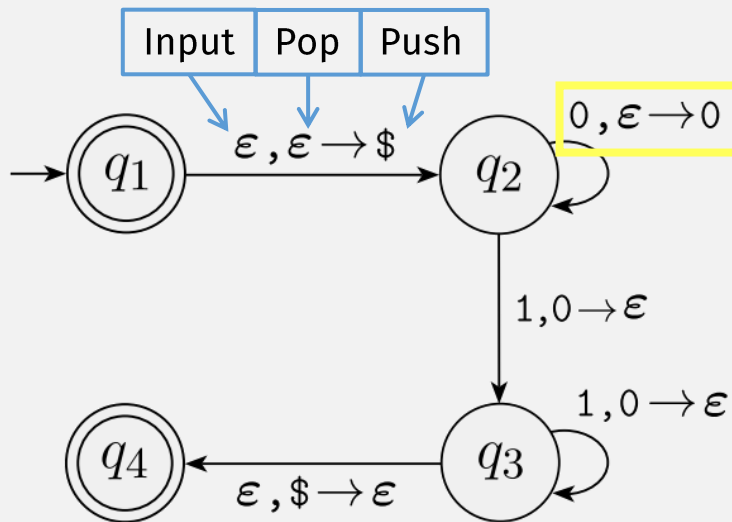
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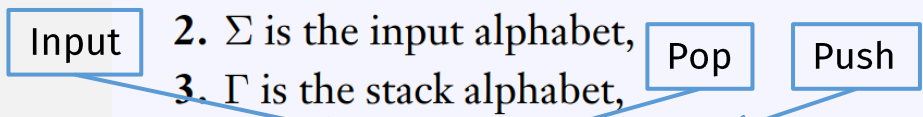
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q_3				$\{(q_3, \epsilon)\}$					$\{(q_4, \epsilon)\}$
q_4									



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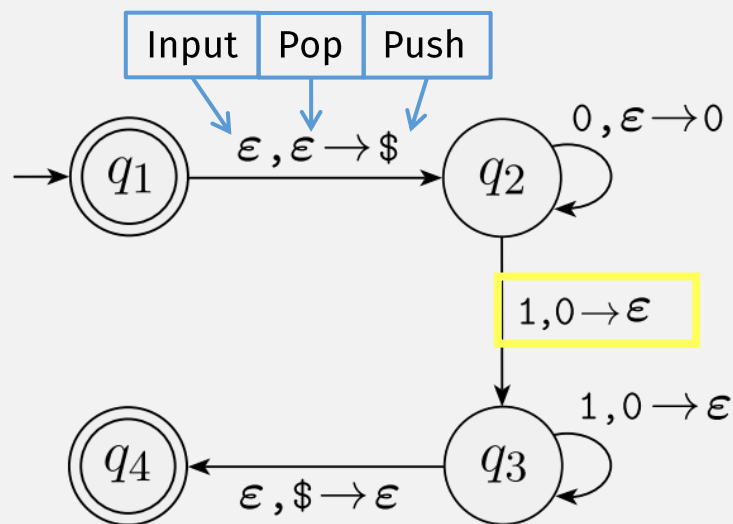
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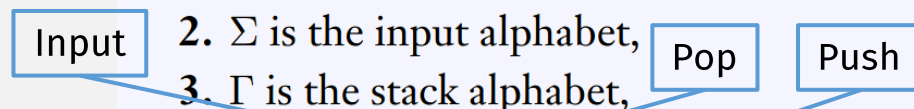
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Stack:	0	\$	ϵ	0	\$	ϵ	0	\$	ϵ
q_1									
q_2			$\{(q_2, 0)\}$			$\{(q_3, \epsilon)\}$			
q_3			1			$\{(q_3, \epsilon)\}$			
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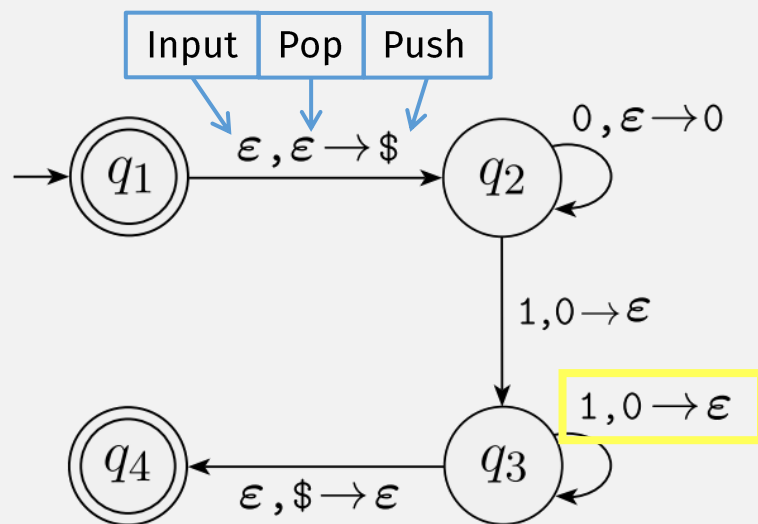
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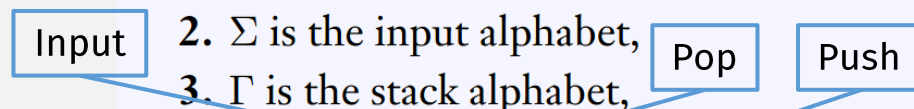
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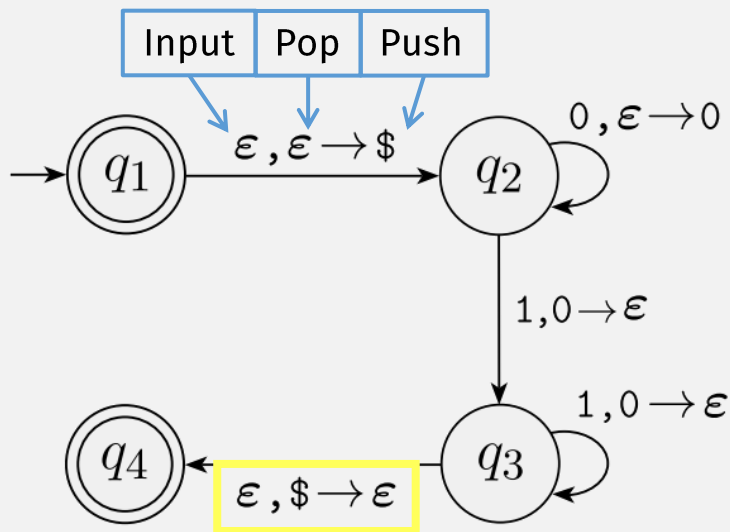
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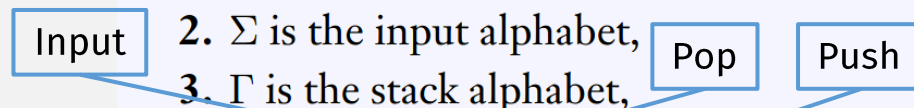
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Flashback: DFA Computation Model

Informally

- Computer = a DFA
 - Program = input string of chars, e.g. "1101"
- To run a program:
- Start in "start state"
 - Read 1 char at a time, changing states according to the transition table
 - Result =
 - "Accept" if last state is "Accept" state
 - "Reject" otherwise

Formally (i.e., mathematically)

- $M = (Q, \Sigma, \delta, q_0, F)$
- $w = w_1 w_2 \cdots w_n$
- $r_0 = q_0$
- $\delta(r_i, w_{i+1}) = r_{i+1}$, for $i = 0, \dots, n - 1$
- M **accepts** w if sequence of states r_0, r_1, \dots, r_n in Q exists ... with $r_n \in F$

For DFA, a single state represents a "snapshot" of the computation

Sequence of states completely represents a computation

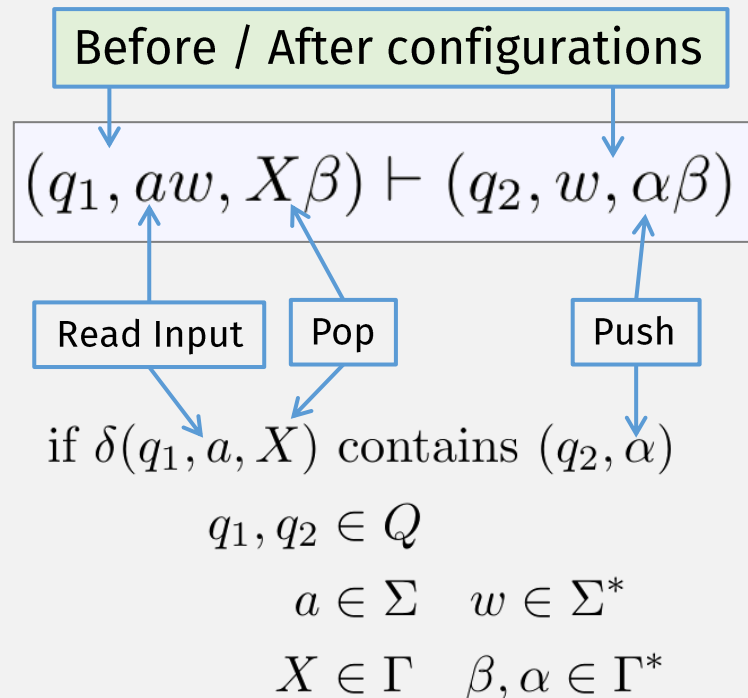
PDA Configurations (IDs)

- A **configuration** (or **ID**) is a “snapshot” of a PDA’s computation
- A configuration (or **ID**) (q, w, γ) has three components:
 - q = the current state
 - w = the remaining input string
 - γ = the stack contents
- A sequence of configurations represents a PDA computation

PDA Computation, Formally

$$P = (Q, \Sigma, \Gamma, \delta, q_0, F)$$

Single-step



Extended

- Base Case

$$I \vdash^* I \text{ for any ID } I$$

- Recursive Case

$$I \vdash^* J \text{ if there exists some ID } K$$
$$\text{such that } I \vdash K \text{ and } K \vdash^* J$$

A configuration (q, w, γ) has three components

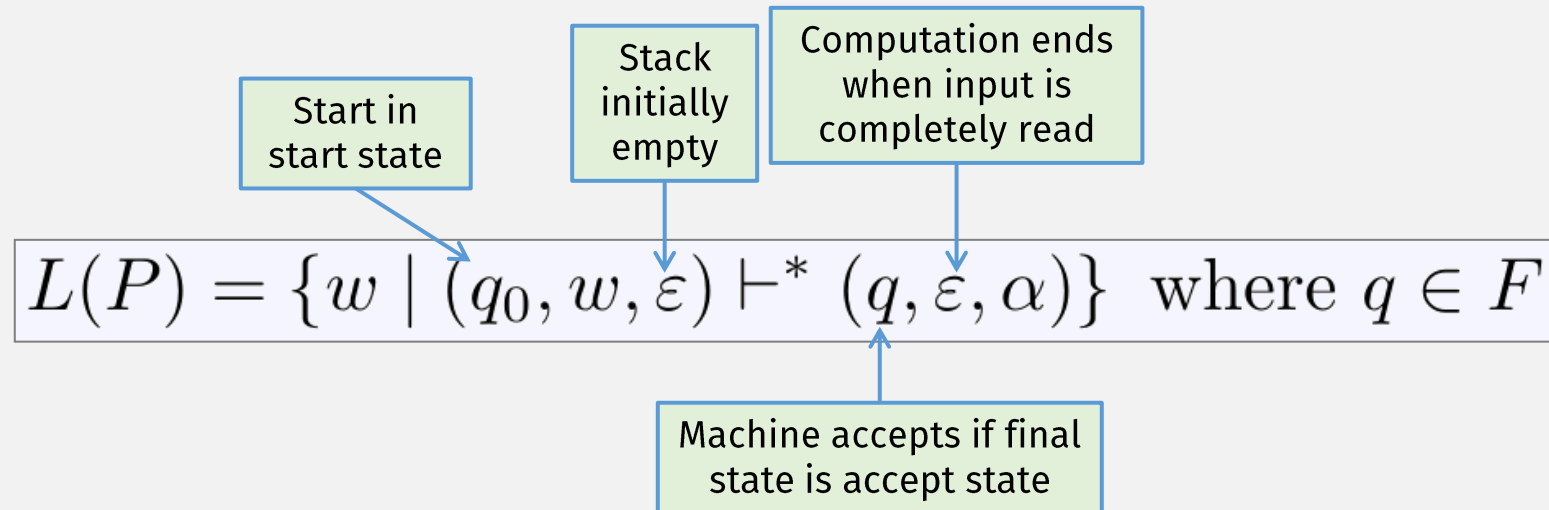
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Language of a PDA

$$P = (Q, \Sigma, \Gamma, \delta, q_0, F)$$



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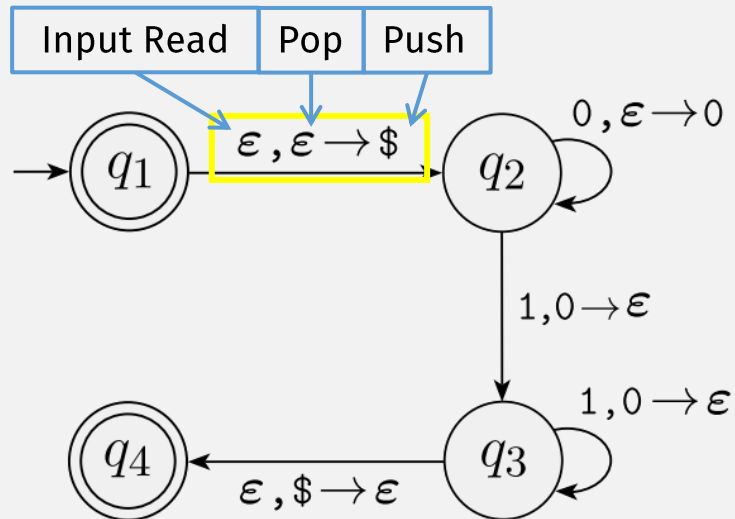
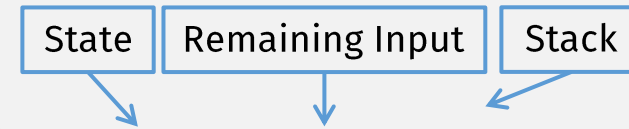
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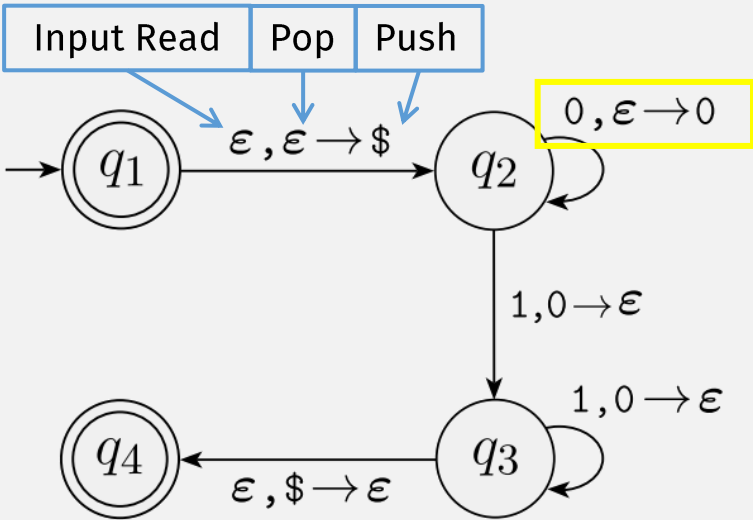
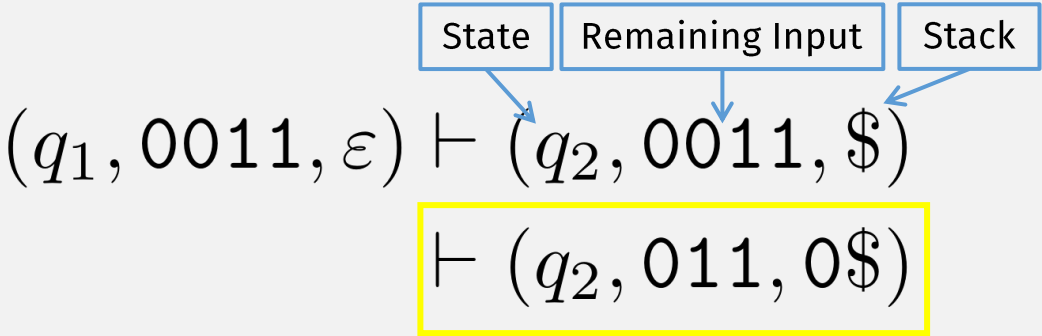
γ = the stack contents

PDA Running Input String Example

$(q_1, 0011, \epsilon)$



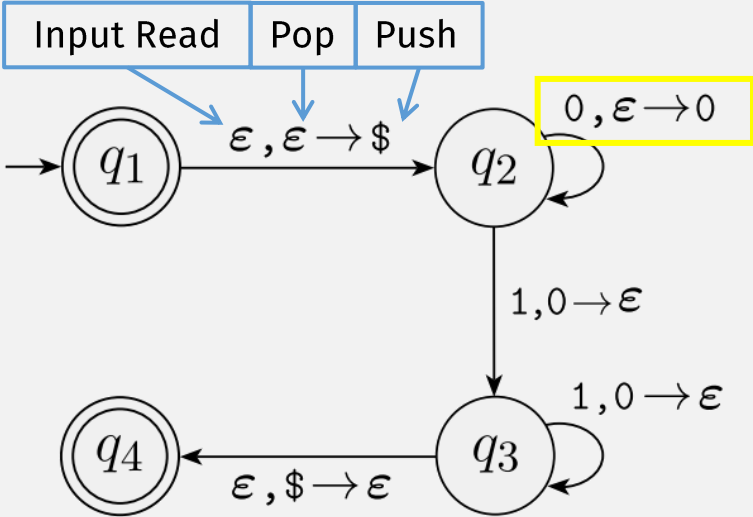
PDA Running Input String Example



PDA Running Input String Example

State	Remaining Input	Stack
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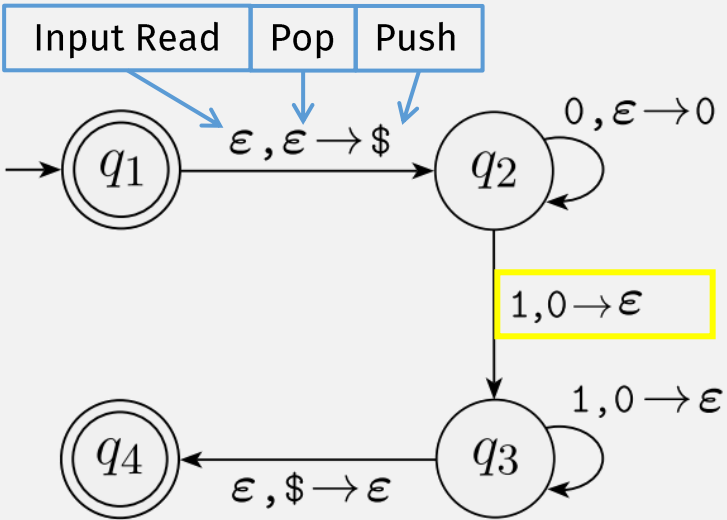
$(q_1, 0011, \epsilon) \vdash (q_2, 0011, \$)$
 $\vdash (q_2, 011, 0\$)$
 $\vdash (q_2, 11, 00\$)$



PDA Running Input String Example

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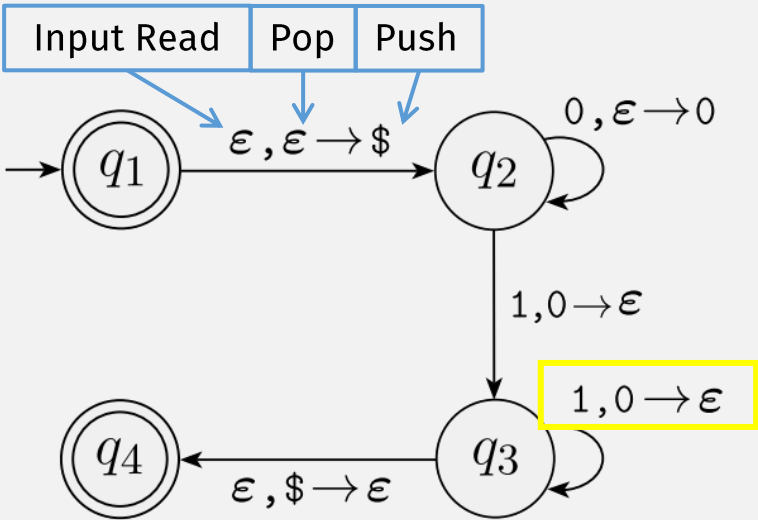
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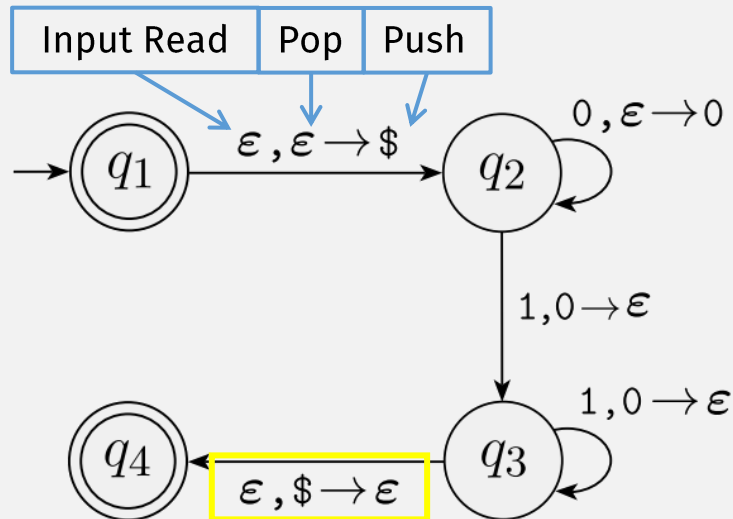
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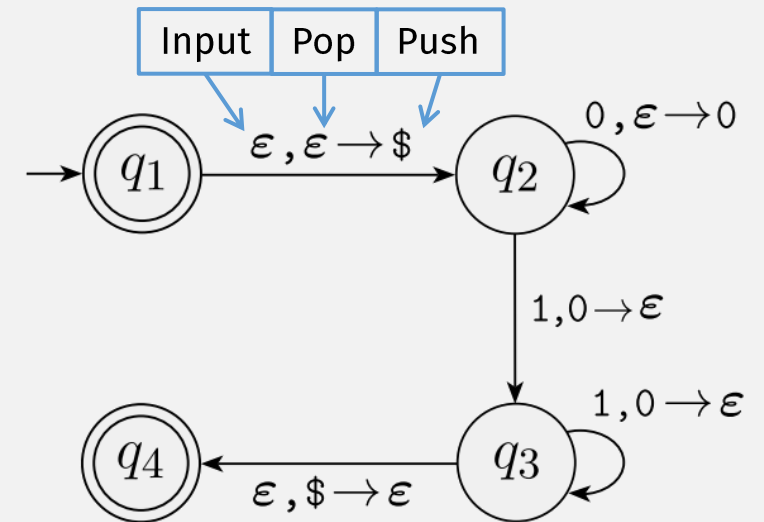
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$\vdash (q_2, 0011, \$)$		
$\vdash (q_2, 011, 0\$)$		
$\vdash (q_2, 11, 00\$)$		
$\vdash (q_3, 1, 0\$)$		
$\vdash (q_3, \epsilon, \$)$		
$\vdash (q_4, \epsilon, \epsilon)$		

Pushdown Automata (PDA)

- PDA = NFA + a stack
 - Infinite memory
 - Can only read/write top location: Push/pop
- Want to prove: PDAs represent CFLs!
- We know: a CFL, by definition, is a language that is generated by a CFG
- Need to show: PDA \Leftrightarrow CFG
- Then, to prove that a language is a CFL, we can either:
 - Create a CFG, or
 - Create a PDA



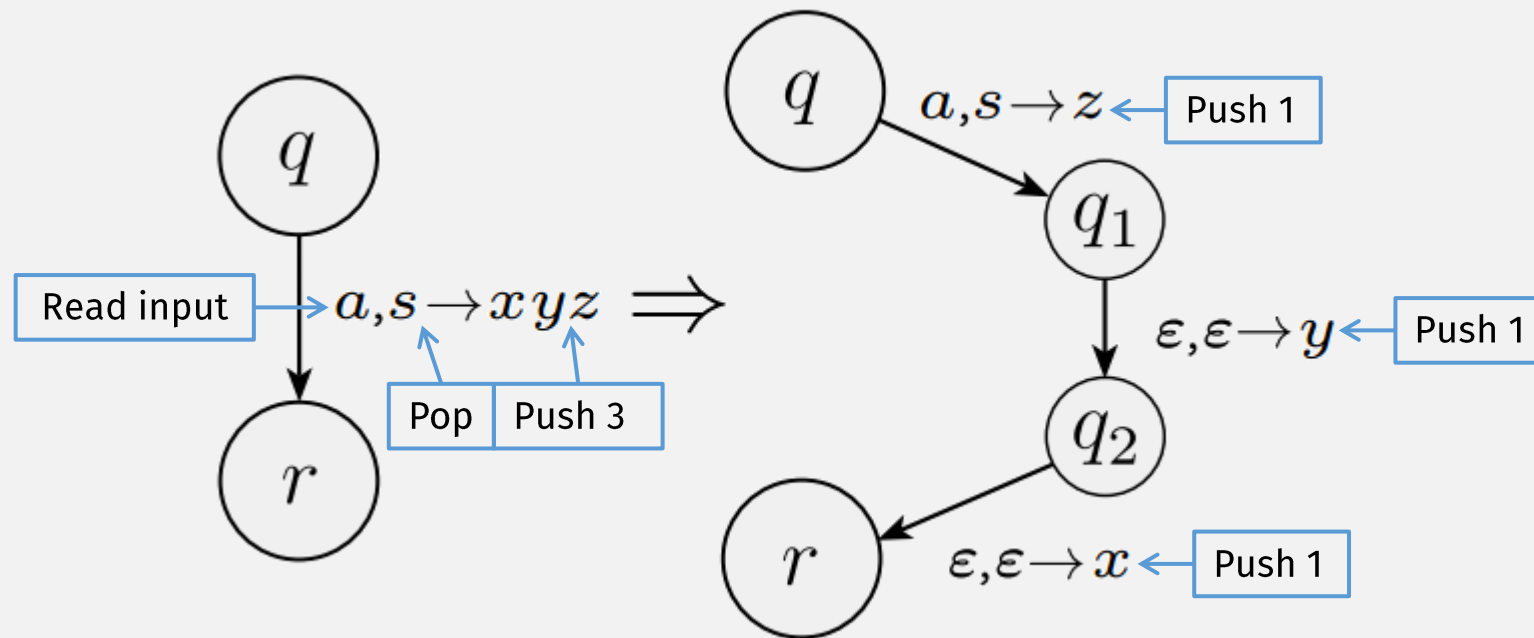
A lang is a CFL iff some PDA recognizes it

⇒ If a language is a CFL, then a PDA recognizes it

- (Easier)
- We know: A CFL has a CFG describing it (definition of CFL)
- Must show: the CFG has an equivalent PDA

⇐ If a PDA recognizes a language, then it's a CFL

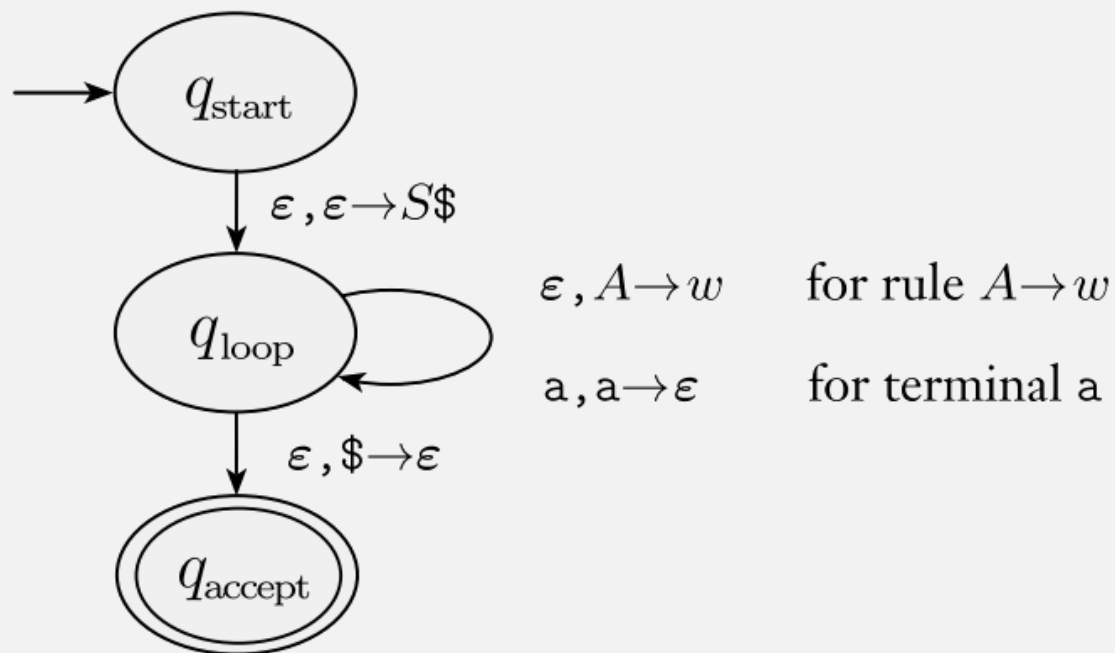
Shorthand: Multi-Symbol Stack Pushes



Note the reverse order of pushes

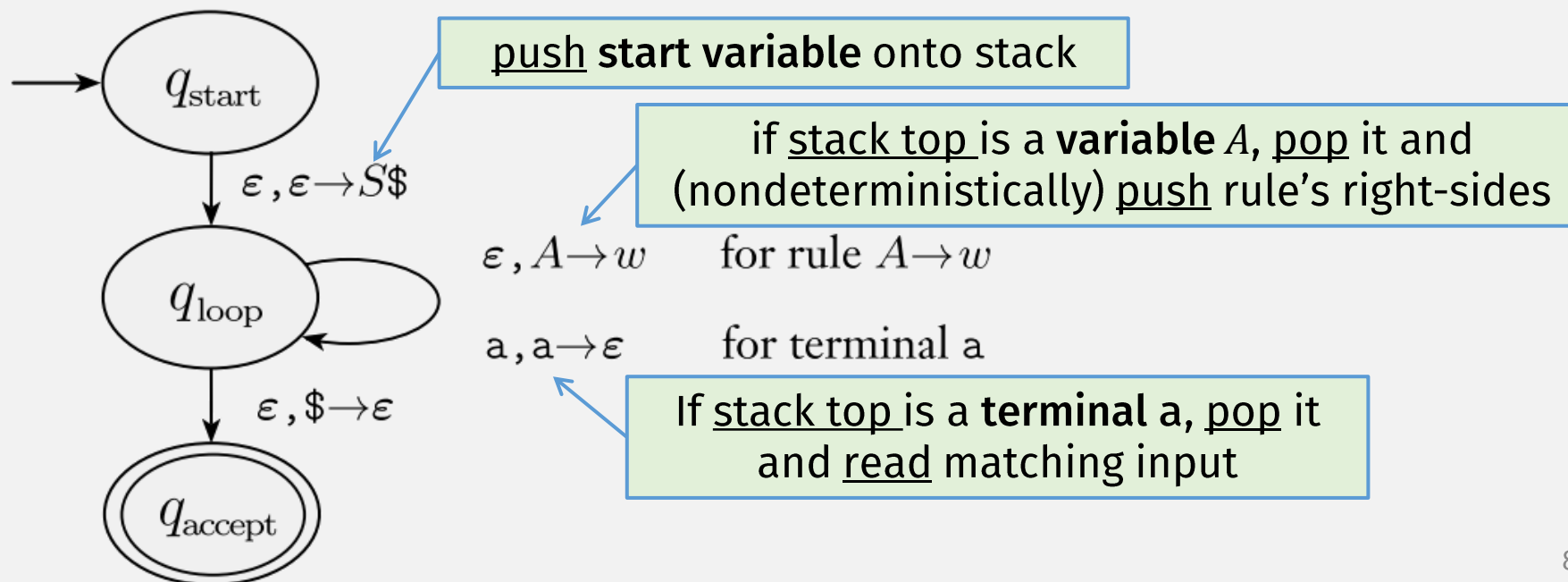
CFG \rightarrow PDA (sketch)

- Construct a PDA from CFG such that:
 - PDA accepts input string only if the CFG can generate that string
- Intuitively, PDA will nondeterministically try all rules

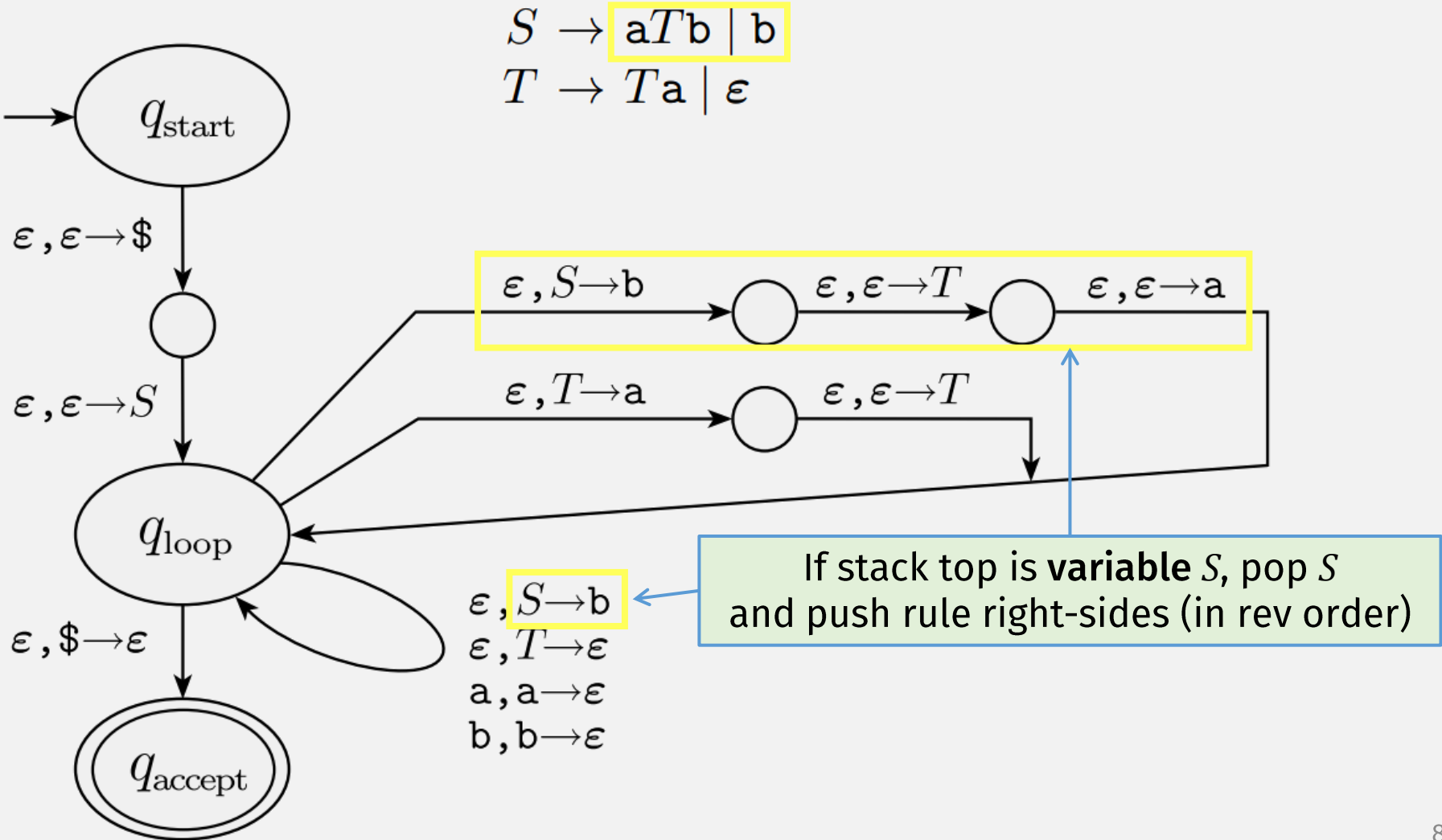


CFG \rightarrow PDA (sketch)

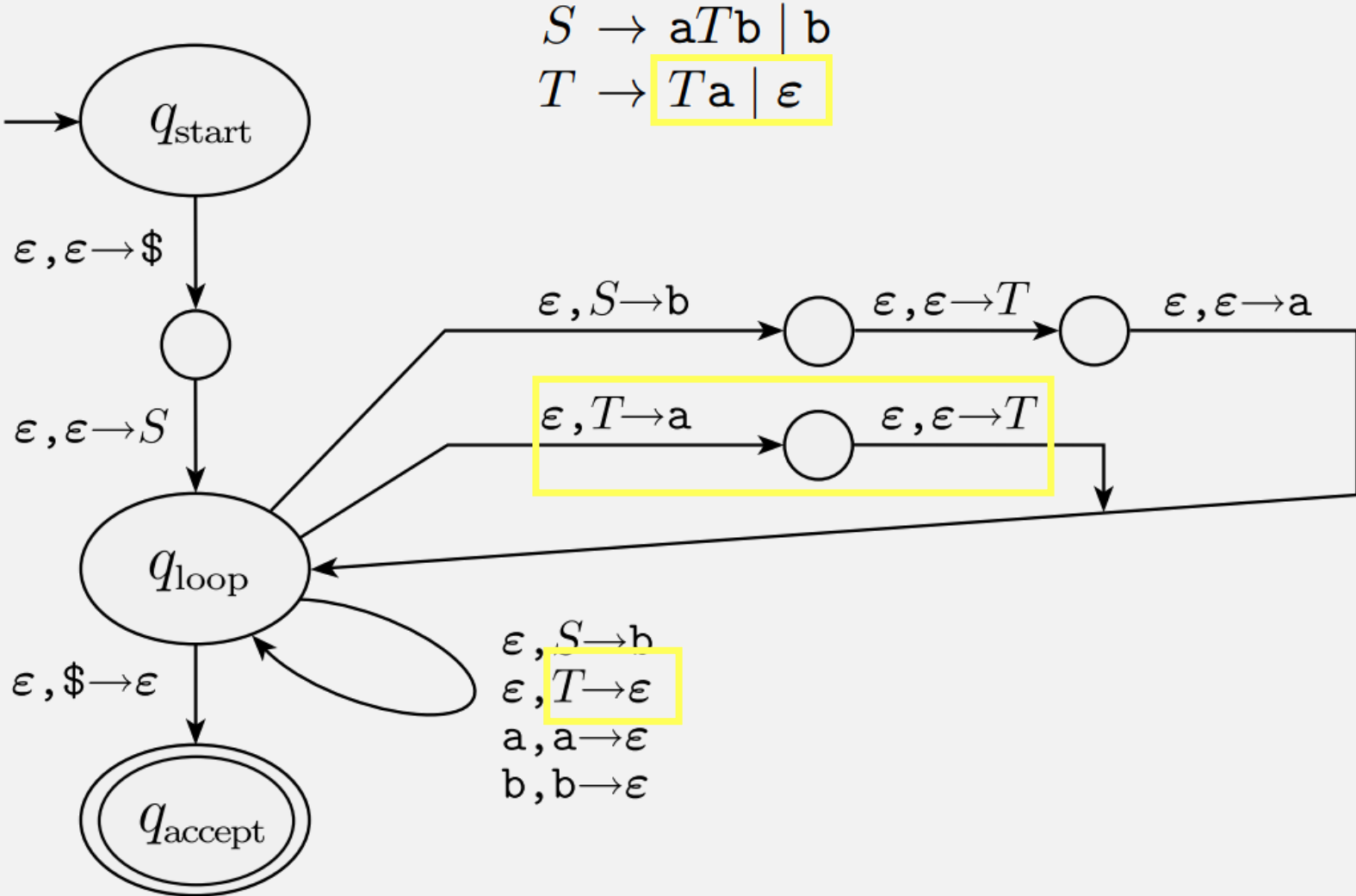
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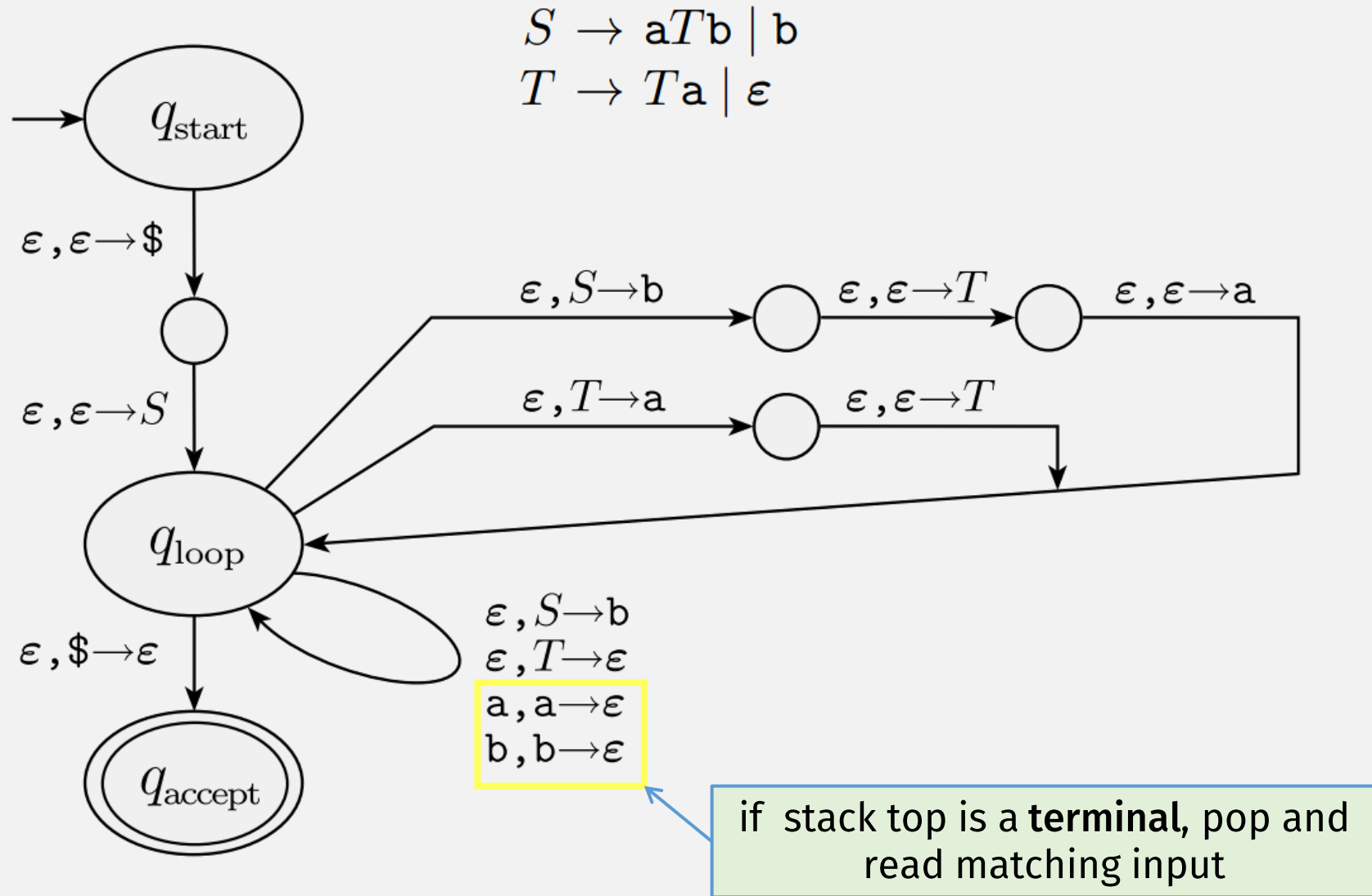
Example CFG \rightarrow PDA



Example CFG \rightarrow PDA

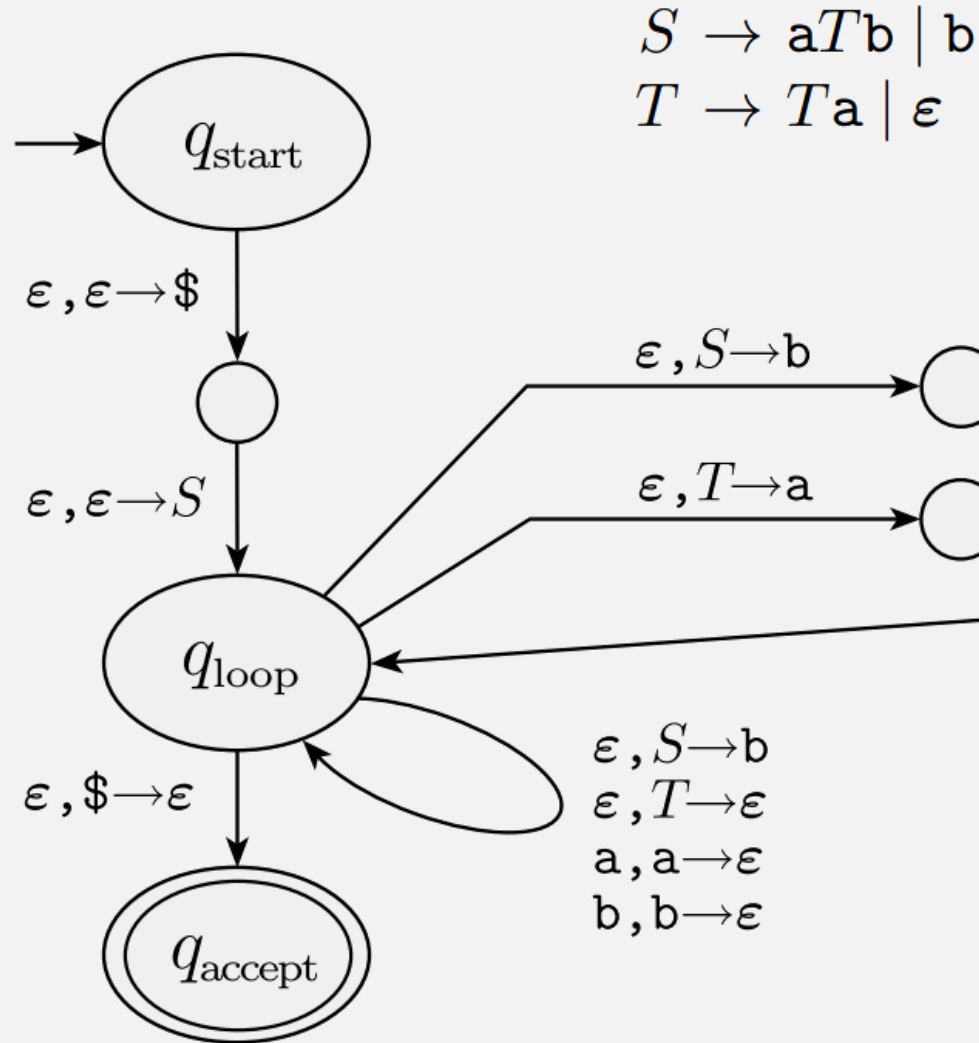


Example CFG \rightarrow PDA



Example CFG \rightarrow PDA

Example Derivation using CFG:
 $S \Rightarrow aTb$ (using rule $S \rightarrow aTb$)
 $\Rightarrow aTab$ (using rule $T \rightarrow Ta$)
 $\Rightarrow aab$ (using rule $T \rightarrow \epsilon$)



PDA Example

State	Input	Stack	Equiv Rule
q_{start}	aab		
q_{loop}	aab	S\$	
q_{loop}	aab	aTb\$	$S \rightarrow aTb$
q_{loop}	ab	Tb\$	
q_{loop}	ab	Tab\$	$T \rightarrow Ta$
q_{loop}	ab	ab\$	$T \rightarrow \epsilon$
q_{loop}	b	b\$	
q_{loop}		\$	
q_{accept}			

Example CFG \rightarrow PDA

Example Derivation using CFG:

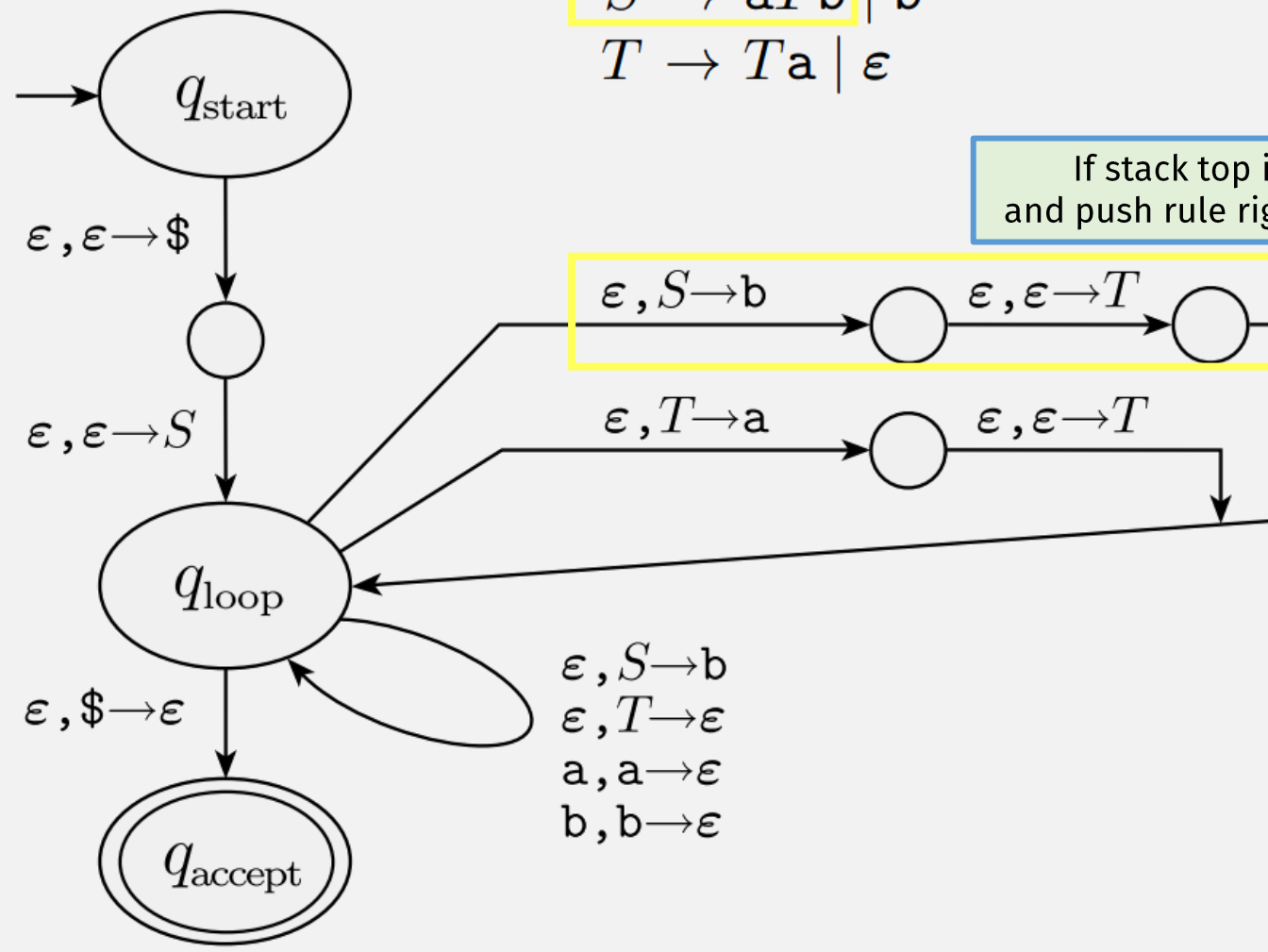
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$\Rightarrow aTab$ (using rule $T \rightarrow Ta$)

$\Rightarrow aab$ (using rule $T \rightarrow \epsilon$)

$S \rightarrow aTb \mid b$
 $T \rightarrow Ta \mid \epsilon$

If stack top is **variable S**, pop S and push rule right-sides (in rev order)



PDA Example

State	Input	Stack	Equiv Rule
q_{start}	aab		
q_{loop}	aab	S\$	
q_{loop}	aab	aTb\$	$S \rightarrow aTb$
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q_{loop}	ab	ab\$	$T \rightarrow \epsilon$
q_{loop}	b	b\$	
q_{loop}		\$	
q_{accept}			

Example CFG \rightarrow PDA

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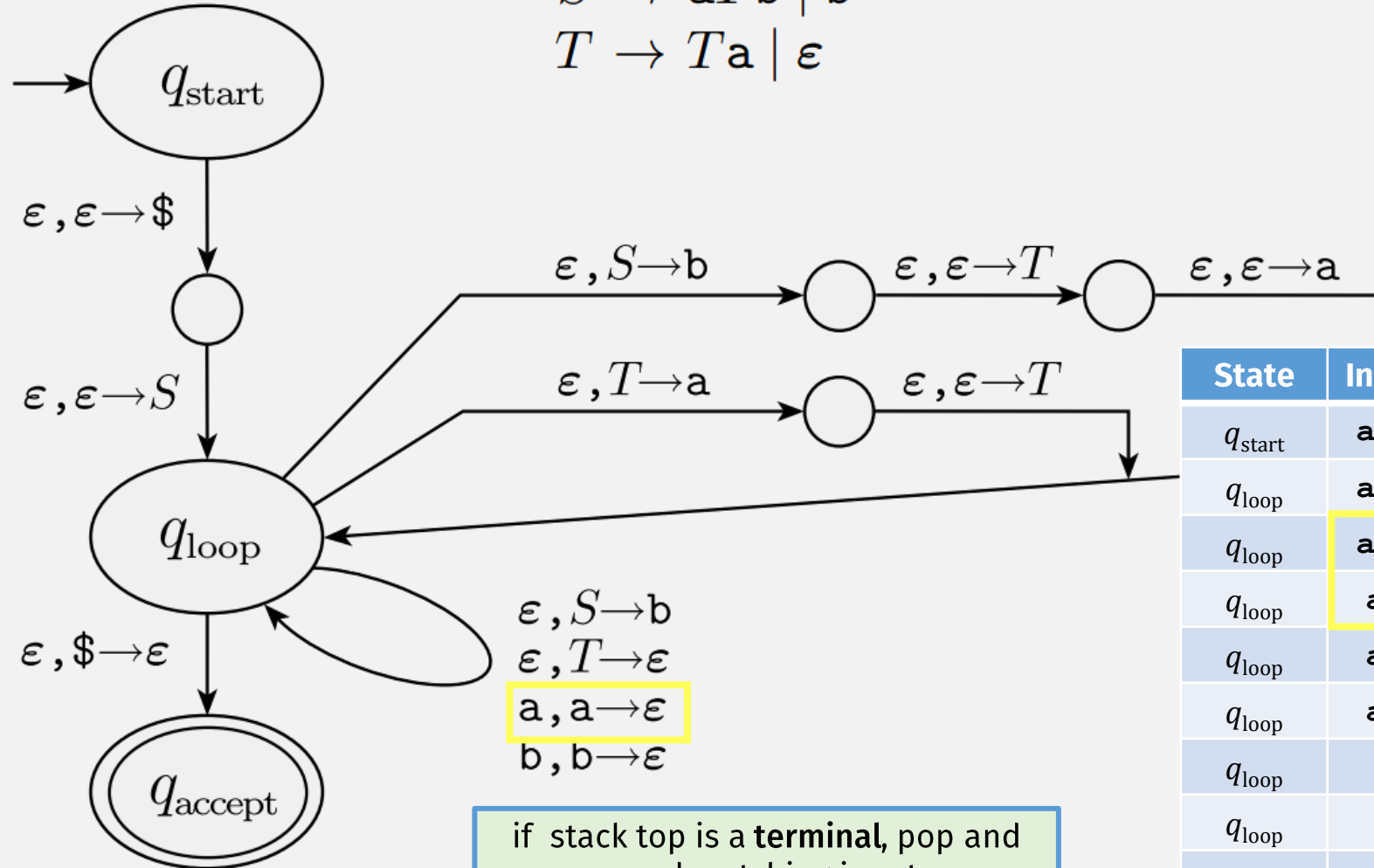
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$T \rightarrow Ta \mid \epsilon$



PDA Example

State	Input	Stack	Equiv Rule
q_{start}	aab		
q_{loop}	aab	$S\$$	
q_{loop}	aab	$aTb\$$	$S \rightarrow aTb$
q_{loop}	ab	$Tb\$$	
q_{loop}	ab	$Tab\$$	$T \rightarrow Ta$
q_{loop}	ab	$ab\$$	$T \rightarrow \epsilon$
q_{loop}	b	$b\$$	
q_{loop}		$\$$	
q_{accept}			

if stack top is a terminal, pop and read matching input

A lang is a CFL iff some PDA recognizes it

\Rightarrow If a language is a CFL, then a PDA recognizes it

- Convert CFG \rightarrow PDA

\Leftarrow If a PDA recognizes a language, then it's a CFL

- (Harder)
- Must Show: PDA has an equivalent CFG

PDA→CFG: Prelims

Before converting PDA to CFG, modify it so :

1. It has a single accept state, q_{accept} .
2. It empties its stack before accepting.
3. Each transition either pushes a symbol onto the stack (a *push* move) or pops one off the stack (a *pop* move), but it does not do both at the same time.

Important:

This doesn't change the language recognized by the PDA

PDA P \rightarrow CFG G : Variables

$P = (Q, \Sigma, \Gamma, \delta, q_0, \{q_{\text{accept}}\})$ variables of G are $\{A_{pq} \mid p, q \in Q\}$

- Want: if P goes from state p to q reading input x , then some A_{pq} generates x
- So: For every pair of states p, q in P , add variable A_{pq} to G
- Then: connect the variables together by,
 - Add rules: $A_{pq} \rightarrow A_{pr}A_{rq}$, for each state r
 - These rules allow grammar to simulate every possible transition
 - (We haven't added input read/generated terminals yet)
- To add terminals: pair up stack pushes and pops (essence of a CFL)⁹⁷

PDA $P \rightarrow$ CFG G : Generating Strings

$P = (Q, \Sigma, \Gamma, \delta, q_0, \{q_{\text{accept}}\})$

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- The key: pair up stack pushes and pops (essence of a CFL)

if $\delta(p, a, \epsilon)$ contains (r, u) and $\delta(s, b, u)$ contains (q, ϵ) ,

put the rule $A_{pq} \rightarrow aA_{rs}b$ in G

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A language is a CFL \Leftrightarrow A PDA recognizes it

\Rightarrow If a language is a CFL, then a PDA recognizes it

- Convert CFG \rightarrow PDA

\Leftarrow If a PDA recognizes a language, then it's a CFL

- Convert PDA \rightarrow CFG



Check-in Quiz 2/23

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